

Listing of the Claims

1. (Currently Amended) A system for reversing degraded energy resolution of semiconductor radiation detection elements ~~(44)~~ which are used in a radiation detector assembly, the system comprising:

a means ~~(38)~~ for identifying semiconductor elements which exhibit degraded energy resolution as compared to an initial level of energy resolution; and

a means ~~(40)~~ for restoring the degraded semiconductor elements to the initial level of energy resolution.

2. (Currently Amended) The system as set forth in claim 1, wherein the semiconductor elements ~~(44)~~ include an array of crystals selected from one of cadmium-zinc-telluride crystals and cadmium-telluride crystals.

3. (Currently Amended) The system as set forth in claim 1, wherein the restoring means ~~(40)~~ further includes:

a reverse bias means ~~(72)~~ which applies a reverse bias to the identified degraded elements for a preselected time.

4. (Currently Amended) The system as set forth in claim 3, wherein the restoring means ~~(40)~~ further includes:

a heater ~~(74)~~, which supplies an elevated ambient temperature, when the reversed bias is applied to the identified degraded elements, to accelerate the recovery of the degraded elements.

5. (Currently Amended) The system as set up forth in claim 1, wherein the identifying means ~~(38)~~ includes a screening means ~~(48)~~ which facilitates identifying degradable semiconductor elements in a batch of new semiconductor elements, the screening means ~~(48)~~ including:

a forward bias means ~~(50)~~, which applies a forward bias to the semiconductor elements to induce a degradation of energy resolution; and

a heater ~~(52)~~, which increases an ambient temperature to accelerate the degradation of the energy resolution in the new semiconductor elements.

6. (Currently Amended) The system as set forth in claim 5, wherein the restoring means ~~(40)~~ further includes:

a reverse bias means ~~(72)~~ which applies a reverse bias to the identified degraded elements for a preselected time.

7. (Currently Amended) The system as set forth in claim 6, wherein the restoring means ~~(40)~~ further includes:

a heater ~~(74)~~, which supplies an elevated ambient temperature, when the reversed bias is applied to the identified degraded elements, to accelerate the recovery of the degraded elements.

8. (Currently Amended) The system as set forth in claim 1, wherein the semiconductor elements ~~(44)~~ of the detector assembly are responsive to gamma radiation.

9. (Original) A method of restoring a degraded performance of semiconductor elements comprising:

identifying semiconductor elements which exhibit degraded energy resolution as compared to an initial level of energy resolution; and

restoring degraded semiconductor elements to the initial level of energy resolution.

10. (Original) The method as set forth in claim 9, wherein in response to identifying a semiconductor with reduced energy resolution, a service call is placed and the step of restoring is performed by a service technician.

11. (Original) The method as set forth in claim 9, wherein restoring includes one of replacing the identified degraded semiconductor elements and restoring the identified semiconductor elements.

12. (Original) The method as set forth in claim 9, wherein during manufacture or assembly, the identified degradable semiconductor elements are grouped based on identified degradation criteria and groups with common degradation criteria are installed in a detector assembly and the restoring step is applied uniformly to the detector assembly.

13. (Original) An apparatus for restoring performance of semiconductor elements including:

an identifying processor to detect elements with degraded performance which results after application of forward bias; and

a restoration processor which controls restoration of energy resolution of degraded elements to an initial level of energy resolution.

14. (Original) The apparatus as set forth in claim 13, wherein the identifying processor includes:

a pixel analyzer which analyzes a response of each semiconductor element when the semiconductor elements are subjected to a radiation source, which pixel analyzer determines a spectral response of each semiconductor element.

15. (Original) The apparatus as set forth in claim 14, wherein the identifying means further includes:

a performance analyzer which compares a spectral response of each semiconductor element with preselected spectral response criteria and calculates energy resolution of each semiconductor element.

16. (Original) The apparatus as set forth in claim 15, wherein the identifying means further includes:

a threshold processor which compares a calculated energy resolution for each semiconductor element with a predetermined energy resolution for each semiconductor element to identify non-degraded and degraded semiconductor elements.

17. (Original) The apparatus as set forth in claim 16, further including:

a reverse bias circuitry to restore the performance of degraded semiconductor elements, which reverse bias circuitry restores the performance by supplying the reverse bias to the identified degraded semiconductor elements for a preselected time.

18. (Original) The apparatus as set forth in claim 17, further including:

a heater to raise an ambient temperature to a preselected level when the reverse bias is supplied to the degraded semiconductor elements to accelerate the restoration process.

19. (Original) The apparatus as set forth in claim 18, further including:

a timer which monitors the application of the forward bias to the semiconductor elements for a preselected time and automatically engages the reverse bias circuitry and the heater to restore the identified degraded semiconductor elements, wherein the reverse bias circuitry and the heater are engaged for a predetermined time.

20. (Original) The apparatus as set forth in claim 13, wherein the forward bias is selectively supplied to semiconductor elements being selected for testing, which forward bias induces degraded performance in the tested semiconductor elements and further including:

a temperature control unit which increases an ambient temperature to accelerate performance degradation in the tested semiconductor elements.

21. (Original) The apparatus as set forth in claim 13, wherein the semiconductor elements include an array of crystals selected from one of cadmium-zinc-telluride crystals and cadmium-telluride crystals.

22. (New) A system for reversing degraded energy resolution of semiconductor radiation detection elements which are used in a radiation detector assembly, the system comprising:

a means for identifying semiconductor elements which exhibit degraded energy resolution as compared to a selectable threshold level of energy resolution; and

a means for restoring the degraded semiconductor elements to an energy resolution above the threshold level.

23. (New) The system as set forth in claim 22, wherein the restoring means further includes a reverse bias means which applies a reverse bias to the identified degraded elements for a preselected time.

24. (New) A medical imaging system comprising:

at least one detector comprising a plurality of semiconductor elements;

a means for identifying semiconductor elements which exhibit degraded energy resolution as compared to a selectable threshold level of energy resolution; and

a means for restoring the degraded semiconductor elements to an energy resolution above the threshold level.

25. (New) The medical imaging system as set forth in claim 24, wherein the restoring means further includes a reverse bias means which applies a reverse bias to the identified degraded elements for a preselected time.

26. (New) The medical imaging system as set forth in claim 24 further comprising screening initiation means that signals a start of a semiconductor element identification process to determine semiconductor elements which exhibit degraded energy resolution.

27. (New) The medical imaging system as set forth in claim 26 wherein the screening initiation means starts the identification process in response to a predetermined time interval, a manual maintenance initiation signal, or a system downtime signal.